

**EAST Search History****EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	0	"726".ccls.	USPAT	OR	OFF	2010/05/23 15:32
L6	930	726/3.ccls.	USPAT	OR	OFF	2010/05/23 15:32

**EAST Search History (I nterference)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1148	709/200.ccls.	USPAT; UPAD	OR	OFF	2010/05/23 15:31
L2	2541	709/201.ccls.	USPAT; UPAD	OR	OFF	2010/05/23 15:31
L3	1678	709/250.ccls.	USPAT; UPAD	OR	OFF	2010/05/23 15:31
L7	1084	726/3.ccls.	USPAT; UPAD	OR	OFF	2010/05/23 15:33
L8	451	726/11.ccls.	USPAT; UPAD	OR	OFF	2010/05/23 15:33
L9	326	712/28.ccls.	USPAT; UPAD	OR	OFF	2010/05/23 15:33
L10	441	712/32.ccls.	USPAT; UPAD	OR	OFF	2010/05/23 15:33
L18	0	(protect\$5 with microchip \$3).clm. and 1	USPAT; UPAD	OR	OFF	2010/05/23 15:37
L19	0	(protect\$5 with microchip \$3).clm. and 2	USPAT; UPAD	OR	OFF	2010/05/23 15:37
L20	0	(protect\$5 with microchip \$3).clm. and 3	USPAT; UPAD	OR	OFF	2010/05/23 15:37

L21	0	(protect\$5 with microchip \$3).clm. and 4	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L22	0	(protect\$5 with microchip \$3).clm. and 7	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L23	0	(protect\$5 with microchip \$3).clm. and 8	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L24	0	(protect\$5 with microchip \$3).clm. and 9	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L25	0	(protect\$5 with microchip \$3).clm. and 10	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L26	0	(protect\$5 with chip\$3). clm. and 1	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L27	0	(protect\$5 with chip\$3). clm. and 2	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L28	0	(protect\$5 with chip\$3). clm. and 3	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L29	0	(protect\$5 with chip\$3). clm. and 4	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L31	0	(protect\$5 with chip\$3). clm. and 7	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L32	0	(protect\$5 with chip\$3). clm. and 8	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L33	0	(protect\$5 with chip\$3). clm. and 9	USPAT; UPAD	OR	OFF	2010/05/23 15:38

L34	1	(protect\$5 with chip\$3).clm. and 10	USPAT; UPAD	OR	OFF	2010/05/23 15:38
L35	3	(protect\$5 with processor \$1).clm. and 1	USPAT; UPAD	OR	OFF	2010/05/23 15:39
L36	8	(protect\$5 with processor \$1).clm. and 2	USPAT; UPAD	OR	OFF	2010/05/23 15:39
L37	6	(protect\$5 with processor \$1).clm. and 3	USPAT; UPAD	OR	OFF	2010/05/23 15:39
L38	13	(protect\$5 with processor \$1).clm. and 7	USPAT; UPAD	OR	OFF	2010/05/23 15:39
L39	11	(protect\$5 with processor \$1).clm. and 8	USPAT; UPAD	OR	OFF	2010/05/23 15:39
L40	4	(protect\$5 with processor \$1).clm. and 9	USPAT; UPAD	OR	OFF	2010/05/23 15:40
L41	1	(protect\$5 with processor \$1).clm. and 10	USPAT; UPAD	OR	OFF	2010/05/23 15:40
L45	0	(secur\$5 with processor\$1 with \$5chip \$3).clm. and 1	USPAT; UPAD	OR	OFF	2010/05/23 15:41
L46	0	(secur\$5 with processor\$1 with \$5chip \$3).clm. and 2	USPAT; UPAD	OR	OFF	2010/05/23 15:42

L48	0	(protect\$5 with microchip \$3).clm. and 7	USPAT; UPAD	OR	OFF	2010/05/23 15:42
L49	0	(protect\$5 with microchip \$3).clm. and 8	USPAT; UPAD	OR	OFF	2010/05/23 15:42
L50	0	(protect\$5 with microchip \$3).clm. and 9	USPAT; UPAD	OR	OFF	2010/05/23 15:42
L51	0	(protect\$5 with microchip \$3).clm. and 10	USPAT; UPAD	OR	OFF	2010/05/23 15:42
L52	0	(secur\$5 with processor\$1 with \$5chip \$3).clm. and 3	USPAT; UPAD	OR	OFF	2010/05/23 15:42
L53	2	(secur\$5 with processor\$1 with \$5chip \$3).clm. and 7	USPAT; UPAD	OR	OFF	2010/05/23 15:42
L54	0	(secur\$5 with processor\$1 with \$5chip \$3).clm. and 8	USPAT; UPAD	OR	OFF	2010/05/23 15:43
L55	0	(secur\$5 with processor\$1 with \$5chip \$3).clm. and 9	USPAT; UPAD	OR	OFF	2010/05/23 15:43
L56	0	(secur\$5 with processor\$1 with \$5chip \$3).clm. and 10	USPAT; UPAD	OR	OFF	2010/05/23 15:43

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